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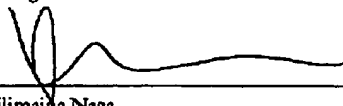
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<b>ATTENTION:</b>	<i>Examiner:</i> LAMARRE, GUY I _____ <i>Art Unit:</i> 2133 _____	

<b>FROM:</b>		<b>TELEPHONE NO.:</b>
Michael J. Ure, Reg. No. 33,089		(408) 474 - 9077
<b>RE:</b>	<i>Serial No.:</i> 09/932,086 _____ <i>Attorney Docket No.:</i> CH000018US _____	

TRANSMISSION INCLUDES:

2 Pages (including cover sheet)

Notice of Appeal - 1 page

<b>CERTIFICATE OF TRANSMISSION UNDER 37 CFR 1.8</b>	
I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office at the number listed above	
on <u>3/26/07</u> 2007	by <u></u> Vilimaina Naga

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

First-Named Inventor: GEORG FARKAS

Application No.: 09/932,086 Conf.: 5457

Date Filed: 08/17/2001

Atty Docket No.: CH000018US

Art Unit: 2133

Examiner: LAMARRE, GUY I

Title: ARRANGEMENT FOR TESTING INTEGRATED CIRCUITS

Commissioner for Patents  
P.O. Box 1450  
Arlington, VA 22313-1450

NOTICE OF APPEAL

Sir:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the decision dated 09/26/2006 of the Examiner finally rejecting claims 2, 4, 10-14.

☒ Please charge the fee of \$500.00 to Deposit Account No. 50-4019.


☐ No additional fee is required, because the fee was paid in a prior appeal.

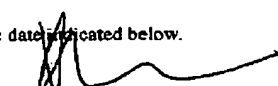
Please charge any fees which may be required, except the issue fee, or credit any overpayment to Deposit Account No. 50-4019.

Date: MARCH -26-07

Respectfully submitted;

By

  
Michael J. Ure, Reg. No. 33,089  
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